

Nanoscale physics

Analysis of optimal barrier configurations for self-shunted Josephson junctions

P. Febvre¹, M. Belogolovskii^{2,3}, E. Zhitlukhina², O. Prokopenko⁴

¹ *IMEP-LAHC – CNRS UMR5130, Université Savoie Mont Blanc, 73376 Le Bourget du Lac, France
E-mail: Pascal.Febvre@univ-smb.fr*

² *Faculty of Physics and Technology, Donetsk National University, 600-richchia Str., 21, 21021 Vinnysia-21021, Ukraine.*

³ *Superconducting Electronics Department, Institute for Metal Physics, Natl. Acad. of Sci. of Ukraine. Academician Vernadsky Boulevard, 36, Kyiv-03680, Ukraine.*

⁴ *Faculty of Radio Physics, Electronics and Computer Systems, Taras Shevchenko National University of Kyiv, Kyiv-01601, Ukraine*

The main device of superconducting micro- and nano-electronics is the Josephson junction formed by two superconducting (S) electrodes with an interface which is weak enough to allow only a slight overlap of electron-pair wave functions from the two electrodes. Conventional SIS devices with a nm-thick insulating (I) interlayer are inherently underdamped and exhibit hysteretic response due to capacitive effect, whereas in superconducting digital applications overdamped Josephson junctions with non-hysteretic current-versus-voltage (I - V) characteristics are needed.

In this contribution, we discuss two novel technologies for fabricating self-shunted Josephson junctions of SIS type (I stands for an ultra-thin semiconducting interlayer with embedded metallic nanoscale drops) and SNIS type (N is the normal film of about 100 nm of thickness in contact with a strongly disordered I layer where stochastic distribution of transparencies takes place on a local rather than on a global scale), both resulting in a single-valued I - V curve.

It is shown that the ratio of the critical supercurrent I_c to the excess current can serve as an indicator of the internal structure of a weak link in Josephson junctions. We have calculated temperature dependence $I_c(T)$ for SNIS junctions with an N=Al film in an intermediate state between dirty and clean limits and compared our numerical simulations with experimental data.

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